

**Cryptographic Algorithms On Reconfigurable
Hardware (Signals And Communication Technology) By
Francisco Rodriguez-Henriquez;N.A. Saqib;Arturo Díaz
Pérez**

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Morphosys reconfigurable hardware for cryptography

This paper presents the mapping and performance analysis of the Twofish algorithm on MorphoSys.
MorphoSys is a reconfigurable architecture that can provide high

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